Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for reading data from a memory card that provides non-volatile data storage <u>having an address space</u>, said method comprising:

(a) accessing volume information stored in <u>a portion of</u> a first volume of <u>the address space of the</u> non-volatile data storage ~~of the memory card~~;

(b) determining whether <u>the non-volatile data storage has a single volume address space one</u> or <u>a multiple volume address space</u> ~~multiple volumes are present on the memory card~~ based on the volume information;

(c) operating the memory card <u>by accessing the entire address space of the non-volatile data storage</u> ~~as providing only~~ the <u>single</u> ~~first~~ volume when said determining (b) determines that <u>the single volume address space</u> ~~one volume~~ is present on the memory card~~, the non-volatile data storage of the memory card being assigned to the one volume~~; and

(d) operating the memory card <u>by dividing the address space of the non-volatile data storage into</u> ~~as providing~~ a plurality of volumes when said determining (b) determines that <u>the multiple volume address space is</u> ~~multiple volumes are~~ present on the memory card, one of the plurality of volumes being the first volume~~, and the non-volatile data storage being divided amongst the plurality of volumes~~.

2. (Original) A method as recited in claim 1,
   wherein the memory card includes a switch that has a plurality of switch positions, and
   wherein said operating (d) includes at least:
      (d1) determining a switch position for the switch; and
      (d2) selectively enabling one of the plurality of volumes based on the switch position.

3. (Original) A method as recited in claim 2,
   wherein the switch has at least a first position and a second position, and
   wherein said operating (d) further includes at least:
      (d3) imposing an address offset when the switch is in the second position.

Atty. Dkt. No. SDK1P017/SDK0503.000US   Page 3 of 13   App. No. 10/754,483

4. (Original) A method as recited in claim 3, wherein the address offset enables the memory card to provide more data storage capacity than available with a file system using 16-bit addressing.

5. (Currently Amended) A method as recited in claim 2,
wherein the switch has at least a first position and a second position,
wherein, when the switch position is in the first position and the memory card is operated by dividing the address space of the non-volatile data storage into ~~as providing~~ the plurality of volumes, the first volume of the non-volatile data storage is accessed, and
wherein, when the switch position is in the second position and the memory card is operated by dividing the address space the non-volatile data storage into ~~as providing~~ the plurality of volumes, a second volume of the non-volatile data storage is accessed.

6. (Original) A method as recited in claim 5, wherein the memory card is formatted into either one of a single volume or a pair of volumes, the pair of volumes being the first volume and the second volume.

7. (Original) A method as recited in claim 6, wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the first and second volumes of X/2 gigabytes each as the pair of volumes.

8. (Original) A method as recited in claim 1, wherein said method further comprises:
(e) detecting activation of the memory card, and
wherein said accessing (a), said determining (b), and said operating (c) or (d) are performed once said detecting (e) detects the activation of the memory card.

9. (Original) A method as recited in claim 8, wherein the activation of the memory card occurs upon power-on of the memory card or upon insertion of the memory card into a host device.

10. (Original) A method as recited in claim 1,
wherein the memory card is formatted into a single volume or a plurality of volumes, and
wherein the total non-volatile data storage for the memory card is formatted into the first volume of X gigabytes as the single volume, or formatted into the N volumes of X/N gigabytes each as the plurality of volumes.

Atty. Dkt. No. SDK1P017/SDK0503.000US Page 4 of 13   App. No. 10/754,483

PAGE 6/23 * RCVD AT 6/29/2007 7:17:18 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID:16509618301 * DURATION (mm-ss):06-12

11. (Currently Amended)   A method as recited in claim 1, <u>wherein</u> when said determining (b) determines that <u>the single volume address space</u> ~~one volume~~ is present on the memory card, the first volume <u>has</u> ~~having~~ a FAT-32 file format.

12. (Currently Amended)   A method as recited in claim 1, <u>wherein</u> when said determining (b) determines that <u>the multiple volume address space is</u> ~~multiple volumes are~~ present on the memory card, each of the multiple volumes <u>has</u> ~~having~~ a FAT-16 file format.

13. (Currently Amended)   A memory card capable of being configured as a single partition <u>having</u> ~~of~~ a first size or as multiple partitions <u>each having</u> ~~of~~ a second size, said memory card comprising:

   non-volatile data storage that provides data storage <u>having an address space</u>, said <u>address space</u> ~~non-volatile data storage~~ being configured to include at least <u>a first</u> partition<u>, the first partition including</u> ~~in which~~ partition information ~~describing at least the first partition is stored~~;

   a switch being set in one of a plurality of switch positions; and

   a controller that manages access to the data stored in said non-volatile data storage, wherein said controller <u>is configured to examine</u> ~~examines~~ the partition information stored in said <u>first partition</u> ~~non-volatile data storage~~ to determine whether the single partition or the multiple partitions are being used based on the partition information,

   wherein when said controller determines that the single partition is used, <u>the entire</u> <u>address space of</u> said non-volatile data storage is <u>accessed</u> ~~addressed~~ as a single partition<u>, the first partition being the single partition,</u> and

   wherein when said controller determines that the multiple partitions are being used, <u>the</u> <u>address space of</u> said non-volatile data storage is <u>divided into</u> ~~addressed as~~ multiple partitions, <u>one of the multiple partitions being accessed</u> based on the switch position of said switch.

14. (Original)   A memory card as recited in claim 13, wherein the one or more partitions are volumes.

15. (Original)   A memory card as recited in claim 13, wherein when said controller determines that the single partition is being used on said memory card, the single partition has a FAT-32 file format.

16. (Original) A memory card as recited in claim 13, wherein when said controller determines that the multiple partitions are being used, each of the multiple partitions has a FAT-16 file format.

17. (Original) A memory card as recited in claim 13,

wherein said memory card is formatted into either a single partition or a plurality of partitions, and

wherein the total non-volatile data storage for said memory card is formatted to a first partition of X gigabytes as the single partition, or formatted into the N partitions of X/N gigabytes each as the plurality of partitions.

18. (Original) A memory card as recited in claim 13, wherein said memory card is a FLASH memory device.

19. (Currently Amended) A memory device that provides non-volatile data storage <u>having an address space</u>, said memory device comprising:

means for accessing volume information stored in <u>a portion of</u> a first volume of <u>the address space of</u> non-volatile data storage ~~of said memory device~~;

means for determining whether <u>a single volume address space</u> ~~one~~ or <u>a</u> multiple <u>volume address space is</u> ~~volumes are~~ present on said memory device based on the volume information; and

means for operating said memory <u>device</u> ~~card~~ based on a configuration of said memory device <u>by accessing the entire address space of the non-volatile data storage as a single volume,</u> <u>or by dividing the address space of the non-volatile data storage</u> into ~~the one or~~ multiple volumes <u>based on a determination of</u> ~~that~~ said means for determining ~~determines to be present on said~~ ~~memory device~~.

20. (Currently Amended) A memory device as recited in claim 19,

wherein said means for operating operates said memory device in a first mode when said means for determining determines that <u>the single volume address space</u> ~~one volume~~ is present on said memory device, and

wherein said means for operating operates said memory device in a second mode when said means for determining determines that the multiple <u>volume address space is</u> ~~volumes~~ are present on said memory device.

Atty. Dkt. No. SDK1P017/SDK0503.000US   Page 6 of 13   App. No. 10/754,483

21. (Original) A memory device as recited in claim 20, wherein said memory device further comprises a switch means that permits user selection of one of the first mode and the second mode.

22. (Currently Amended) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing only the first volume when said means for determining determines that ~~the single~~ <u>one</u> volume <u>address space</u> is present on said memory device.

23. (Currently Amended) A memory device as recited in claim 19, wherein said means for operating operates said memory device as providing a plurality of volumes when said means for determining determines that <u>the multiple volume address space is</u> ~~multiple volumes are~~ present on said memory device, one of the plurality of volumes being the first volume, ~~and the non-volatile data storage being divided amongst the plurality of volumes~~.

24. (Original) A memory device as recited in claim 19, wherein said memory device is a memory card.

25. (Original) A memory device as recited in claim 19, wherein said memory device is a FLASH memory device.

26. (Currently Amended) A method for reading data from a memory card that provides non-volatile data storage, said method comprising:
  (a) accessing a switch position of a switch on the memory card;
  (b) determining whether <u>the non-volatile data storage has a single volume address space</u> ~~one~~ or <u>a</u> multiple <u>volume address space</u> ~~volumes are present on the memory card~~ based on the switch position;
  (c) operating the memory card <u>by accessing the entire address space as a single volume</u> as ~~providing only the first volume~~ when said determining (b) determines that <u>the single</u> ~~one~~ volume <u>address space</u> is present on the memory card, ~~the non-volatile data storage of the memory card being assigned to the one volume~~; and
  (d) operating the memory card <u>by dividing the address space into</u> ~~as providing~~ a plurality of volumes when said determining (b) determines that multiple <u>volume address space is</u> ~~volumes~~

Atty. Dkt. No. SDK1P017/SDK0503.000US Page 7 of 13   App. No. 10/754,483

~~are present on the memory card, one of the plurality of volumes being the first volume, and the non-volatile data storage being divided amongst the plurality of volumes.~~

27. (Original) A method as recited in claim 26, wherein the switch has three-positions.

28. (Currently Amended) A memory card capable of being configured as a single partition having ~~of~~ a first size or as multiple partitions <u>each having</u> ~~of~~ a second size, said memory card comprising:

non-volatile data storage <u>having an address space</u> that provides data storage~~, said non-volatile data storage being configured to include at least the single partition that is used to store partition information describing at least the single partition~~;

a switch being set in one of a plurality of switch positions; and

a controller that manages access to the data stored in said non-volatile data storage, wherein said controller <u>is configured to examine</u> ~~examines~~ the switch position of said switch to determine whether the single partition or the multiple partitions are being used ~~based on the partition information~~,

wherein when said controller determines that the single partition is used, <u>the entire</u> <u>address space of</u> said non-volatile data storage is <u>accessed</u> ~~addressed~~ as a single volume, and

wherein when said controller determines that the multiple partitions are being used, <u>the address space of</u> said non-volatile data storage is <u>divided into</u> ~~addressed as~~ multiple partitions<u>, one of the multiple partitions being accessed</u> based on the switch position of said switch.

29. (Original) A memory card as recited in claim 28, wherein the one or more partitions are volumes.

30. (Original) A memory card as recited in claim 28, wherein when said controller determines that the single partition is being used on said memory card, the single partition has a FAT-32 file format.

31. (Original) A memory card as recited in claim 28, wherein when said controller determines that the multiple partitions are being used, each of the multiple partitions has a FAT-16 file format.

Atty. Dkt. No. SDK1P017/SDK0503.000US   Page 8 of 13   App. No. 10/754,483

PAGE 10/23 * RCVD AT 6/29/2007 7:17:18 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/15 * DNIS:2738300 * CSID:16509618301 * DURATION (mm-ss):06-12

32. (Original) A memory card as recited in claim 28,

wherein said memory card is formatted into either a single partition or a plurality of partitions, and

wherein the total non-volatile data storage for said memory card is formatted to a first partition of X gigabytes as the single partition, or formatted into the N partitions of X/N gigabytes each as the plurality of partitions.

33. (Original) A memory card as recited in claim 32, wherein said switch has N+1 switch positions.

34. (Original) A memory card as recited in claim 28, wherein said memory card is a FLASH memory device.

35. (Original) A memory card as recited in claim 32, wherein said switch has three switch positions.